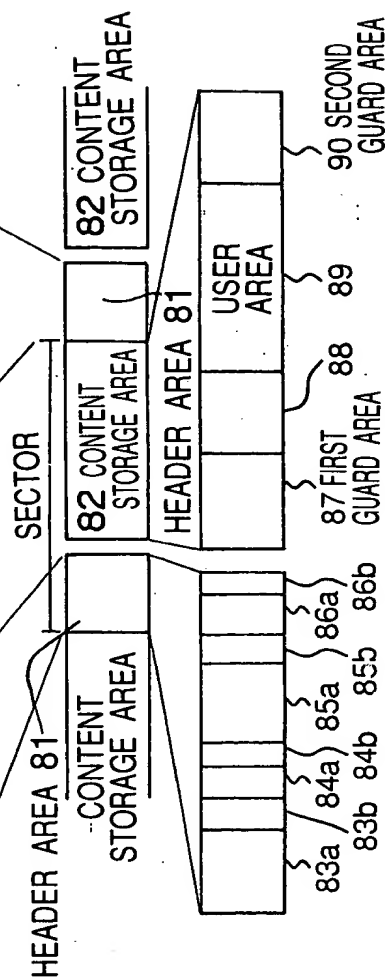


TOSHD-42304860

37

The diagram illustrates the timing of various signals in a digital video recording system. The signals are labeled as follows:

- (a) RF SIGNAL: A complex waveform representing the modulated RF signal.
- (b) TE SIGNAL: A signal with a sharp leading edge followed by a wavy tail, indicating a transition event.
- (c) OUTPUT SIGNAL FROM CLAMPING UNIT: A signal that follows the TE signal, showing the clamping process.
- (d) HEADER GATE SIGNAL: A rectangular pulse signal used to gate the header information.
- (e) READ GATE SIGNAL: A rectangular pulse signal used to gate the read data.
- (f) DIGITIZED WOBBLE SIGNAL: A high-frequency, high-amplitude signal used for timing synchronization.
- (g) SECTOR: A diagram showing the structure of a sector, including:
 - 82 CONTENT STORAGE AREA: The main data storage area.
 - 82 CONTENT STORAGE AREA: A second instance of the main data storage area.
 - HEADER AREA 81: The header information area.
 - 87 FIRST GUARD AREA: A guard area following the header.
 - 88 USER AREA: The user data area.
 - 89 90 SECOND GUARD AREA: A second guard area at the end of the sector.
- (h) ADDRESS INFORMATION: A diagram showing the address information structure, including:
 - 83a, 84a, 85a, 86a: Address information for the first half of the sector.
 - 83b, 84b, 85b, 86b: Address information for the second half of the sector.
 - 83b, 84b, 85b, 86b: Address information for the third half of the sector.
 - 83a, 84a, 85a, 86a: Address information for the fourth half of the sector.
- (i) READ GATE SIGNAL: A rectangular pulse signal used to gate the read data.



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

705210-12304860

Fig.4

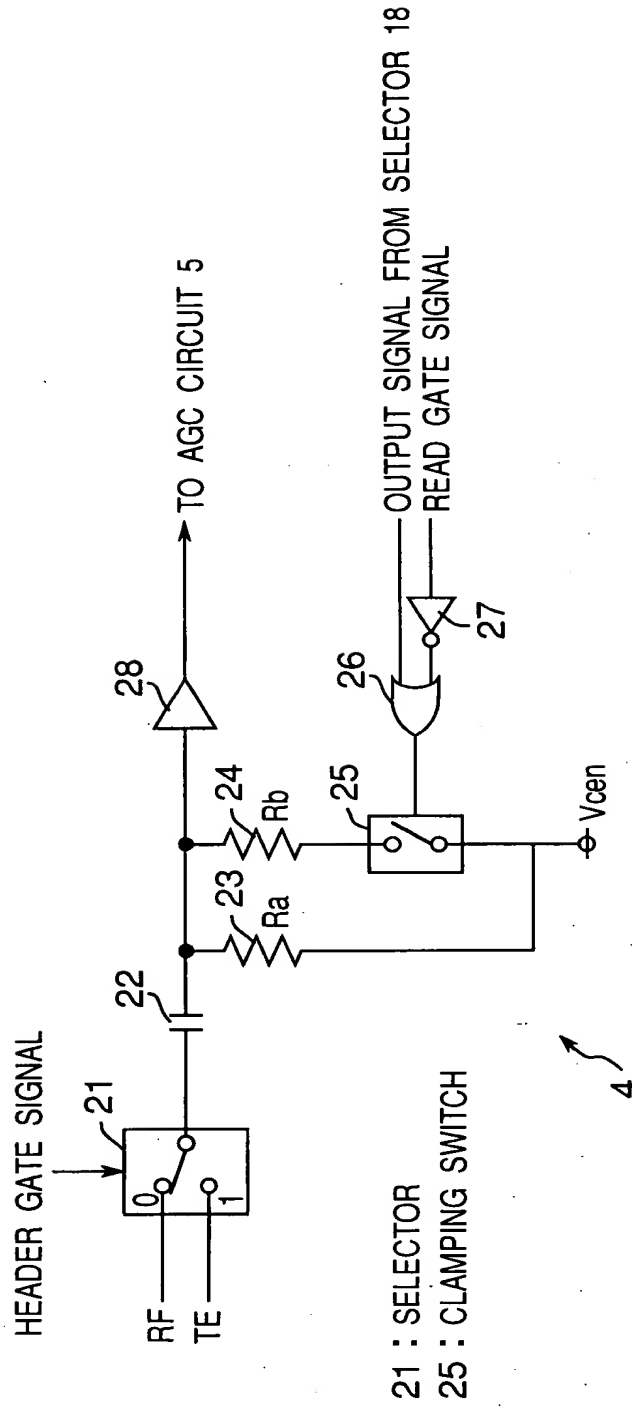
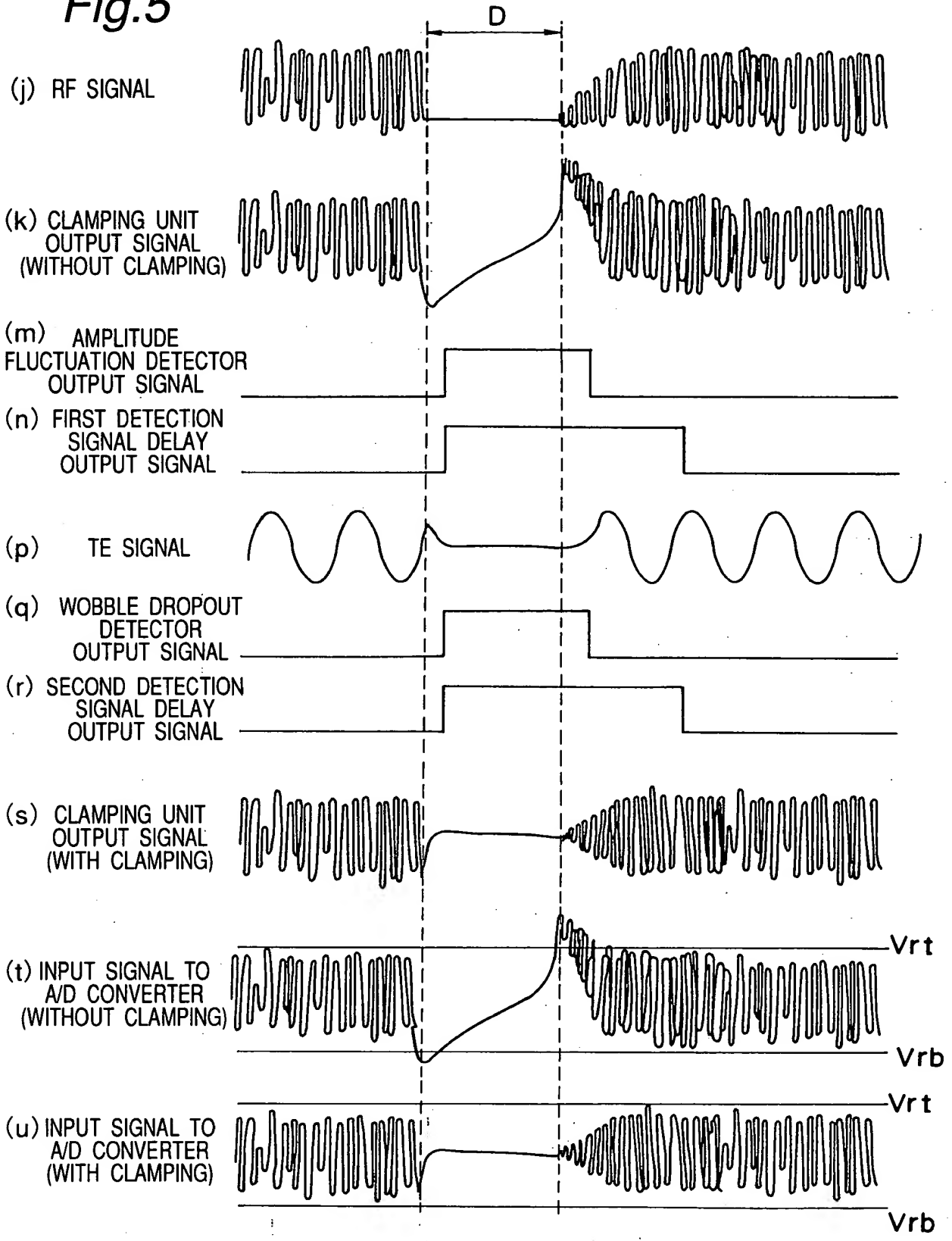
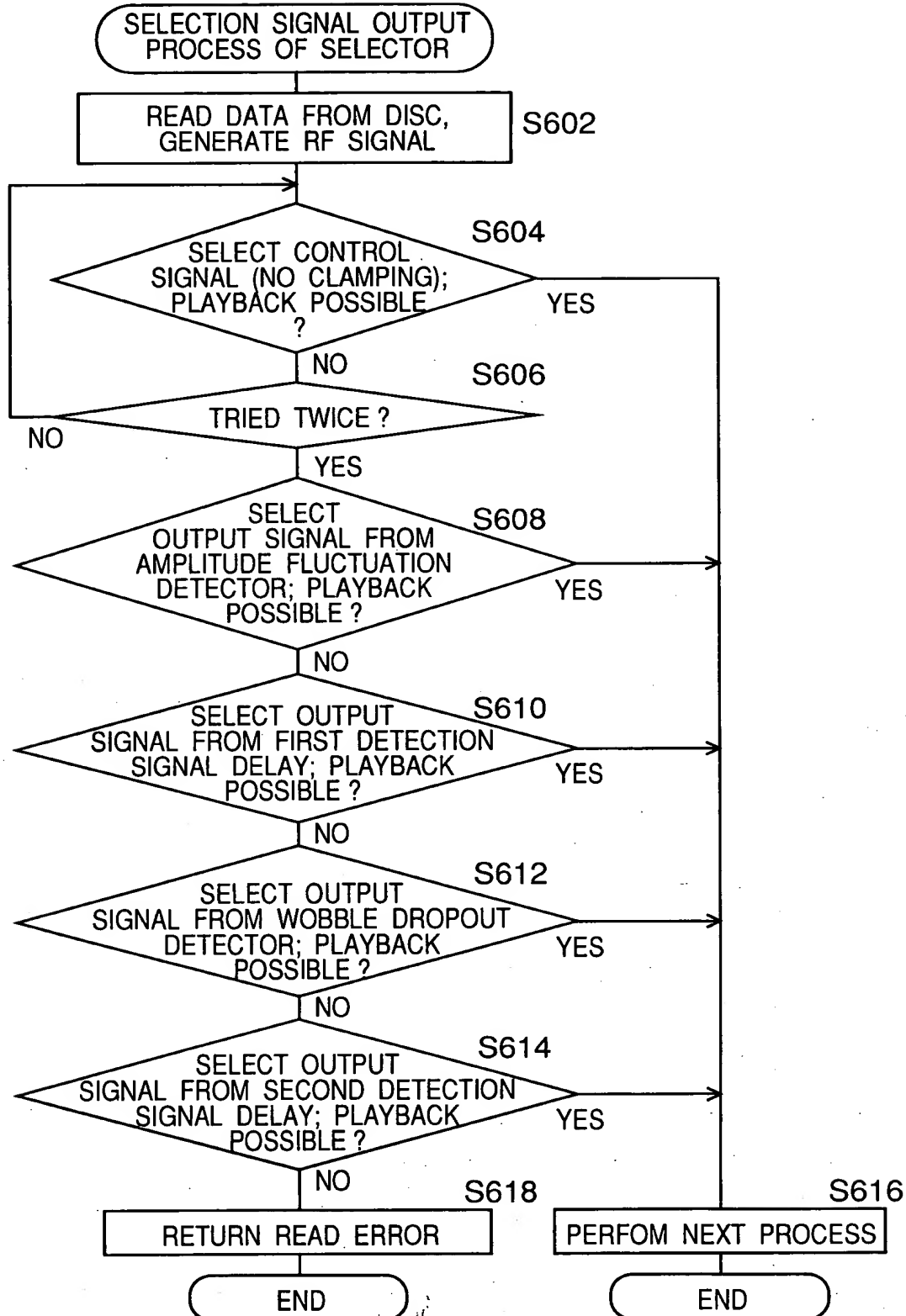


Fig.5



T05240-4304360

Fig.6

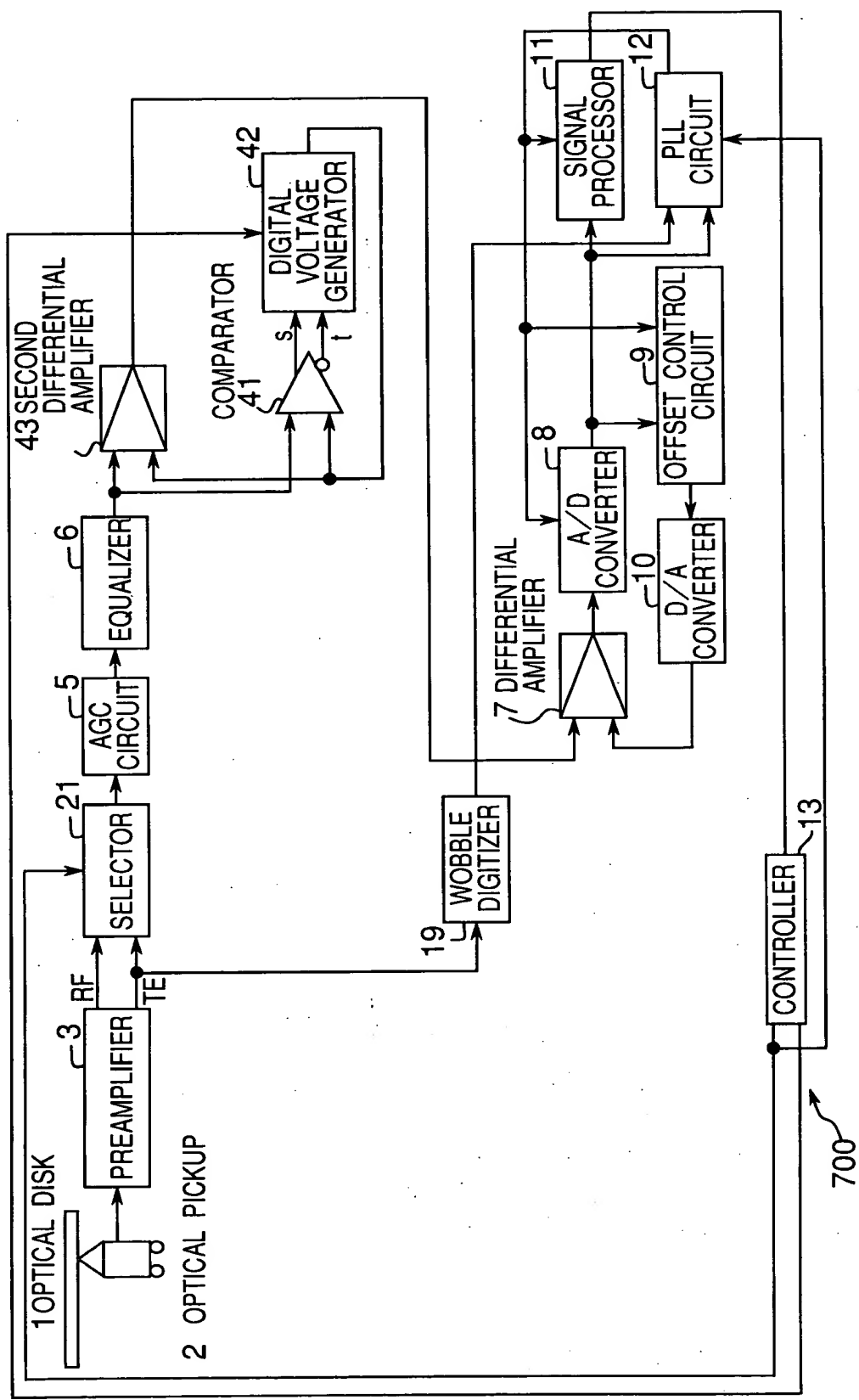


T05210-12801850

FIG. 7

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

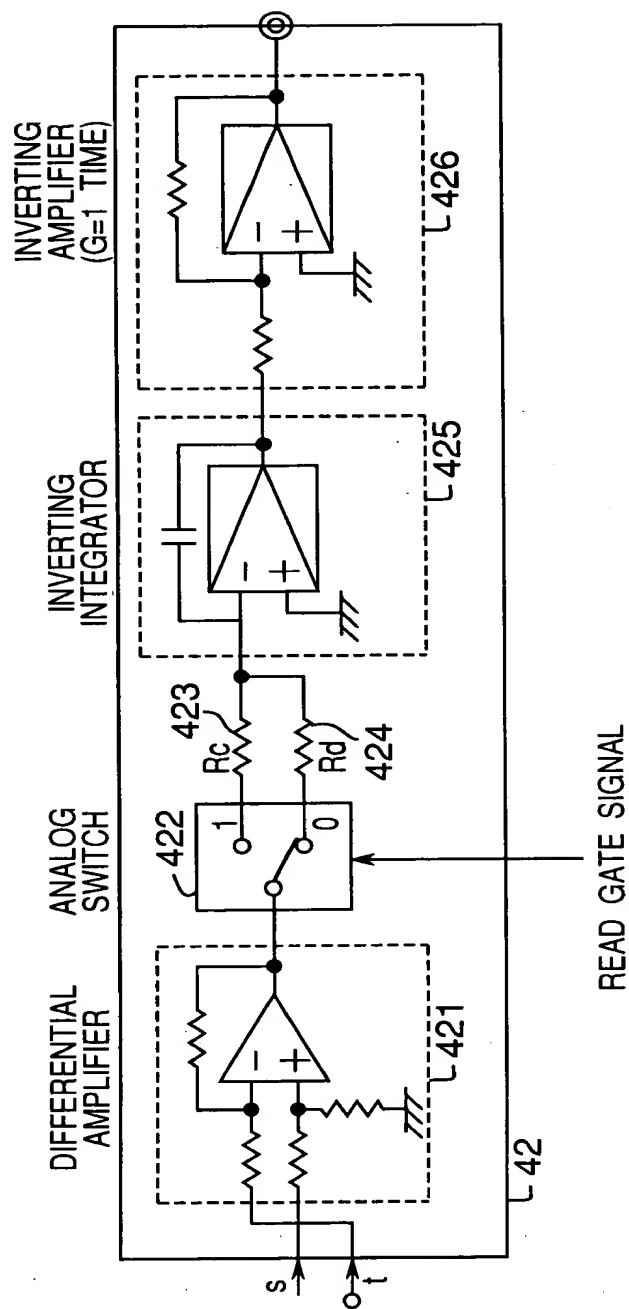
Fig. 7



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 8

Fig. 8



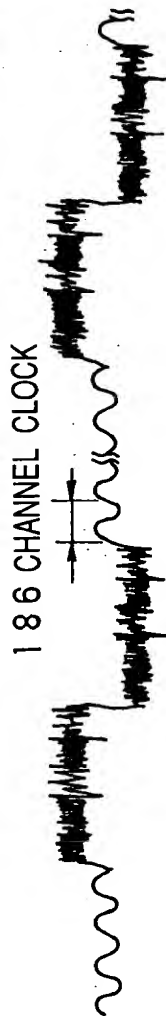
FORM 1-2807860

Fig.9

(a) RF SIGNAL



(b) TE SIGNAL



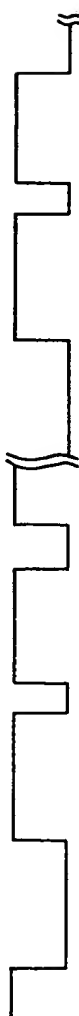
(v) SELECTOR
OUTPUT SIGNAL



(d) HEADER GATE
SIGNAL



(e) READ GATE
SIGNAL



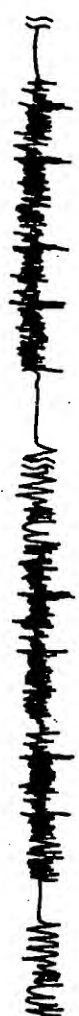
(w) DIGITAL VOLTAGE
GENERATOR
OUTPUT SIGNAL



(x) SECOND DIFFERENTIAL
AMPLIFIER
OUTPUT SIGNAL



(s) COMPATOR
OUTPUT SIGNAL
(V-W)



105210-142804960

Fig. 10

